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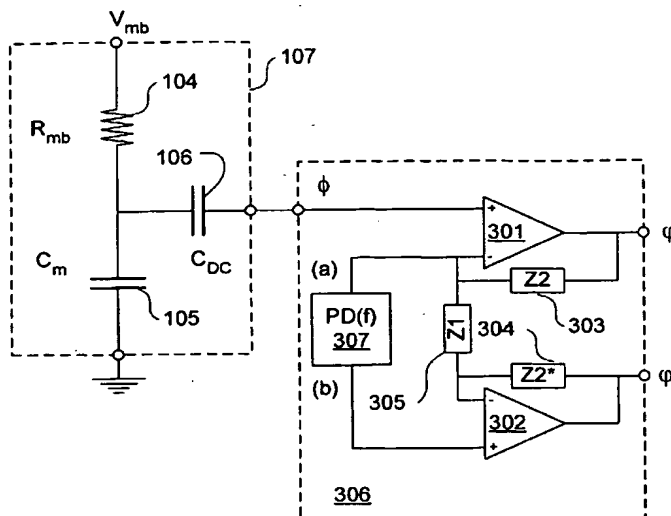
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(54) Title: DIGITAL MICROPHONE



(57) Abstract: An integrated circuit, configured to process microphone signals, where the integrated circuit comprises: a preamplifier (306) with an amplifier section (301) which has a first input (ϕ) and a second input (ϕ^*) and an output (ϕ), and with a feedback filter network ($Z1$; $Z1^*$, $Z2$) coupled between the output (ϕ ; ϕ^*) and the second input (ϕ^*); where the first input (ϕ) to the amplifier section (301) has an input impedance which by means of the input impedance of the amplifier section is substantially isolated from the feedback network with respect to input impedance; and where the preamplifier has a frequency-gain transfer function which suppress low frequencies; and an analogue-to-digital converter coupled to receive an anti-aliasing filtered input signal and providing a digital output signal (Do).



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